


Search Notes 	Application/Control No. 10669095	Applicant(s)/Patent Under Reexamination BUTTS, MICHAEL R.
	Examiner Rossoshek, Helen	Art Unit 2825

Notes	Date	Examiner
EAST - USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	10/31/2005	HR
U.S. Patent and Trademark Office		Part of Paper No.: 10282005

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	0	fpga near3 (emulator near3 (hardware or processor)) and (processor near3 (logic and macro))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 08:14
L4	0	fpga near3 (emulator near3 (hardware or processor)) and (processor same logic\$3 same macro)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 08:15
L5	1	fpga same (emulator near3 (hardware or processor)) and (processor same logic\$3 same macro)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 08:16
L6	1	(emulat\$3 near3 (software or fpga)) near3 (emulator near3 (hardware or processor)) and (processor same logic\$3 same macro)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 08:16
S1	0	(fpga or (programmable adj device)) same ((verif\$3 or test\$3 or evaluat\$) near3 functionaliiy) same processor same memory same logic same macro	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:18
S2	0	(fpga or (programmable adj device)) same ((verif\$3 or test\$3 or evaluat\$) near3 functionaliiy) same processor same memory same logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:18
S3	1	(fpga or (programmable adj device)) same ((verif\$3 or test\$3 or evaluat\$) near3 functionality) same processor same memory same logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:20
S4	1	(fpga or (programmable near3 device)) same ((verif\$3 or test\$3 or evaluat\$) near3 functionality) same processor same memory same logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:21
S5	81	(fpga or (programmable near3 device)) same (verif\$3 or test\$3 or evaluat\$) same function\$5 same processor same memory same logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:22
S6	9	(fpga or (programmable near3 device)) same (verif\$3 or test\$3 or evaluat\$) same function\$5 same processor same memory same logic and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:37

S7	16	emulat\$3 same processor same enabl\$3 same (FPGA or (programmable near3 (device or logic))) same implement\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:41
S8	2	"20040123258"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:46
S9	0	(processot near3 based near3 emulat\$3) same (fpga or (device near3 (logic or programm\$5)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 12:48
S10	10	(processor near3 based near3 emulat\$3) same (fpga or (device near3 (logic or programm\$5)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 13:08
S11	301	(emulat\$3 near3 (software or fpga)) same (emulator near3 (hardware or processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 13:10
S12	207	(emulat\$3 near3 (software or fpga)) near3 (emulator near3 (hardware or processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 08:16
S13	1	(emulat\$3 near3 (software or fpga)) same enabl\$3 same implement\$3 same (emulator near3 (hardware or processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 13:58
S14	17	emulat\$3 same (software or fpga) same enabl\$3 same implement\$3 same emulator same (hardware or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 13:15
S15	17	(emulat\$3 near3 (software or fpga)) same enabl\$3 same (emulator near3 (hardware or processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 14:16
S16	79	fpga same (emulator near3 (hardware or processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 08:15

S17	29	fpga near3 (emulator near3 (hardware or processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 08:14
S18	8	fpga same (emulator near3 (hardware or processor)) same accelerat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 14:17